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PATENT & TRADEMARK OFFICE
S/N 09/633375

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Daniel R. Loughmiller et al. Examiner: Jimmy Nguyen
Serial No.: 09/633375 Group Art Unit: 2829
Filed: August 7, 2000 Docket No.: 303.145US3
Title: CIRCUIT AND METHOD FOR MEASURING AND FORCING AN
INTERNAL VOLTAGE OF AN INTEGRATED CIRCUIT

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on October 24, 2002. Please amend the above-identified patent application as follows.

IN THE DRAWINGS

Corrected drawings are submitted herewith.

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 40, 41, and 45. The specific amendments to individual claims are detailed in the following marked up set of claims.

40. (Amended) A method comprising:

receiving a read signal;

coupling a pass circuit between an internal node and a pin of an integrated circuit;

applying an applied voltage to the pin;

resetting the pass circuit with a reset circuit based on a [second] first state of the read signal; and

passing the applied voltage from the pin to the internal node based a second state of the read signal.

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